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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/498,525	02/04/2000	Hammam Elabd	73234	73234 8921	
7	7590 04/22/2003				
	dison & Sutro LLP	EXAM	EXAMINER		
Ninth Floor Ea	•••	VO, LILIAN			
Washington, DC 20005-3918			ART UNIT	PAPER NUMBER	
			2127	7	
			DATE MAILED: 04/22/2003		

Please find below and/or attached an Office communication concerning this application or proceeding.

-6		,	Application No.	Applicant(s)	T/P
Offic Action Summ			09/498,525	ELABD, HAMMAM	
		Action Summary	Examiner	Art Unit	
			Lilian Vo	2127	
Peri d fo	The MAIL or Reply	LING DATE of this communication a	ppears on the cover sheet with the c	correspondence address	
THE - Exte after - If the - If NO - Failu - Any	MAILING Descriptions of time in SIX (6) MONTH period for reply period for reply re to reply within reply received by	DATE OF THIS COMMUNICATION nay be available under the provisions of 37 CFR HS from the mailing date of this communication. y specified above is less than thirty (30) days, a ru y is specified above, the maximum statutory perion in the set or extended period for reply will. by state	PLY IS SET TO EXPIRE 1 MONTH(1.136(a). In no event, however, may a reply be tin eply within the statutory minimum of thirty (30) day of will apply and will expire SIX (6) MONTHS from ute, cause the application to become ABANDONE ling date of this communication, even if timely filed	nely filed s will be considered timely. the mailing date of this communication (35.1 S.C. 6.133)	n.
1)🖂	Respons	ive to communication(s) filed on 04	<u> 4 February 2000</u> .		
2a)□	This action	on is FINAL . 2b) 🖾 -	This action is non-final.		
3) Disp siti	Since this closed in ion of Clair	accordance with the practice unde	wance except for formal matters, pr er <i>Ex parte Quayle</i> , 1935 C.D. 11, 4	rosecution as to the merits i 53 O.G. 213.	is
4)🖂	Claim(s)	1-32 is/are pending in the application	on.		
	4a) Of the	above claim(s) is/are withdr	awn from consideration.		
5)	Claim(s) _	is/are allowed.			
6)	Claim(s) _	is/are rejected.			
7)	Claim(s) _	is/are objected to.			
8)⊠	Claim(s) 1	-32 are subject to restriction and/o	r election requirement.		
Applicati	on Papers				
9) 🗆 -	The specific	cation is objected to by the Examir	ner.		
10) 🔲 🛚	The drawing	g(s) filed on is/are: a)□ acc	epted or b) objected to by the Exar	miner.	
	Applicant	may not request that any objection to t	the drawing(s) be held in abeyance. Se	ee 37 CFR 1.85(a).	
11) 🔲 🖯	The propos	ed drawing correction filed on	is: a)☐ approved b)☐ disappro	ved by the Examiner.	
i.	If approve	d, corrected drawings are required in r	eply to this Office action.		
12) 🔲 1	The oath or	declaration is objected to by the E	xaminer.	,	
Pri rity u	nder 35 U.	S.C. §§ 119 and 120			
13)	Acknowled	gment is made of a claim for foreig	gn priority under 35 U.S.C. § 119(a))-(d) or (f).	
a)[□All b)□	Some * c) None of:			
	1. Certi	ified copies of the priority documer	nts have been received.		
	2. Certi	ified copies of the priority documer	nts have been received in Applicatio	on No	
	a	application from the International B	ority documents have been received ureau (PCT Rule 17.2(a)). It of the certified copies not received	· ·	
14) 🗌 A	cknowledgi	ment is made of a claim for domes	tic priority under 35 U.S.C. § 119(e) (to a provisional application	on).
15)□ A	cknowledg		rovisional application has been recestic priority under 35 U.S.C. §§ 120		
Attachment		- Cited (DTO 202)	4. □ · · · · •	(DTO 110) D	
2) D Notice	of Draftspers	es Cited (PTO-892) con's Patent Drawing Review (PTO-948) ure Statement(s) (PTO-1449) Paper No(s)	5) Notice of Informal Pa	(PTO-413) Paper No(s) atent Application (PTO-152)	
U.S. Patent and Tra PTO-326 (Rev		Office A	Action Summary	Part of Paper No. 3	3

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DETAILED ACTION

Election/Restrictions

1. Restriction to one of the following inventions is required under 35 U.S.C. 121:

Group I, claims 1 - 14, which drawn to a system for providing parallel processing of data to a plurality of DSPs with program/data memory and ping-pong memory interface, and how the communication data is transmitted, classified in class 709, subclass 250.

Group II, claims 15 – 30, which drawn to a DSP load management system, which also include a decoder for decoding instructions such as load and algorithm switching instructions to provide optimal processing, classified in class 709, subclass 212.

Group III, claims 21 – 30, which drawn to a method of providing parallel processing of data to a plurality of DSPs which write the communication data to a selected register from a plurality of registers, wherein the register is selected based on the current load status and different algorithms loaded, classified in class 709, subclass 200.

Group IV, claims 31 – 32, which drawn to a method of optimizing the processing capabilities of a plurality of digital signal processors on an SOC devices having a DSP load management system with inter-transaction, intra-transaction, and hardware mapping optimizations, classified in class 709, subclass 223.

2. The inventions are distinct, each from the other because of the following reasons: Inventions I - IV are related as subcombinations disclosed as usable together in a single combination. The subcombinations are distinct from each other if they are shown to be

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separately usable. In the instant case, invention I has separate utility such as in a system lacking a decoder for decoding instructions such as load and algorithm switching instructions to provide optimal processing, the writing of the communication data to a selected register from a plurality of registers, wherein the register is selected based on the current load status and different algorithms loaded, and a method of optimizing the processing capabilities of a plurality of digital signal processors on an SOC devices having a DSP load management system with intertransaction, intra-transaction, and hardware mapping optimizations. Invention II has separate utility such as in a system lacking the writing of the communication data to a selected register from a plurality of registers, wherein the register is selected based on the current load status and different algorithms loaded, and a method of optimizing the processing capabilities of a plurality of digital signal processors on an SOC devices having a DSP load management system with inter-transaction, intra-transaction, and hardware mapping optimizations. Invention III has separate utility such as in a system lacking a method of optimizing the processing capabilities of a plurality of digital signal processors on an SOC devices having a DSP load management system with inter-transaction, intra-transaction, and hardware mapping optimizations. See MPEP § 806.05(d).

3. Because these inventions are distinct for the reasons given above and have acquired a separate status in the art as shown by their different classification, restriction for examination purposes as indicated is proper.

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4. Because these inventions are distinct for the reasons given above and the search required

for one group is not required for another group, restriction for examination purposes as indicated

is proper.

5. A telephone call was made to Chang Kim on 4/15/2003 to request an oral election to the

above restriction requirement, but did not result in an election being made.

6. Applicant is advised that the reply to this requirement to be completed must include an

election of the invention to be examined even though the requirement be traversed (37 CFR

1.143).

Any inquiry concerning this communication or earlier communications from the 7.

examiner should be directed to Lilian Vo whose telephone number is 703-305-7864.

Any inquiry of a general nature or relating to the status of this application or proceeding

should be directed to the receptionist whose telephone number is 703-305-3900.

Lilian Vo

Examiner

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April 17, 2003

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